IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Application Serial No | |
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| | January 5, 2001 |
| | Leonard Forbes, et al. |
| Assignee | Micron Technology, Inc. |
| | 2823 |
| Examiner | K.D. Nguyen |
| Attorney's Docket No | Ml22-1531 |
| Title | Methods of Forming Capacitor Structures |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference -See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed together with the filing of a Request for Continued Examination (RCE) Application. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 9-26-06

By:

D. Brent Kenady Reg. No. 40,045 Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MI22-1531

APPLICANT: Leonard Forbes, et al.

SERIAL NO. 09/755,673

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FILING DATE January 5, 2001 GROUP 2823

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.